

AMENDMENTS TO THE CLAIMS

This listing of claims will replace all prior versions and listings of claims in the application;

--1. (Currently Amended) An information processing apparatus comprising:

~~a plurality of~~ receiving means having a plurality of inputs for respectively receiving a request signal for requesting bus acquisition for each of a plurality of modules;

measurement means for measuring a time limit of each of said plurality of modules based on the request signal received by each of said plurality of inputs of said receiving means;

priority determination means for determining a priority of bus acquisition of said plurality of modules according to the time limit measured by said measurement means; and

control means for controlling acquisition of the bus for said plurality of modules based on the priority determined by said priority determination means.

--2. (Currently Amended) The information processing apparatus according to Claim 1, wherein said priority determination means determines priority by means of a round-robin method ~~[[if]]~~ when there is a plurality of modules having a same time limit as measured by said measurement means.

--3. (Currently Amended) An information processing method

comprising the steps of:

receiving a request signal for requesting bus acquisition  
for each of a plurality of modules;

measuring a time limit of each of said plurality of modules  
based on a request signal requesting bus acquisition received for  
each of ~~[[a]]~~ the plurality of modules, ~~for requesting bus~~  
~~acquisition~~;

determining priority of bus acquisition of said plurality of  
modules according to a time limit as measured in said ~~measurement~~  
step of measuring; and

controlling acquisition of the bus for said plurality of  
modules based on the priority as determined in said ~~priority~~  
~~determination~~ step of determining priority.

--4. (Currently Amended) A storage medium for storing a  
computer-readable program for causing the computer to execute the  
steps of:

measuring a time limit of each of said a plurality of  
modules based on a request signal received for each of a  
plurality of modules, for requesting bus acquisition;

determining priority of bus acquisition of said plurality of  
modules according to ~~[[a]]~~ the time limit ~~[[as]]~~ measured in said  
~~measurement~~ step of measuring; and

controlling acquisition of the bus for said plurality of  
modules based on the priority as determined in said ~~priority~~

determination step of determining priority.

--5. (Currently Amended) A computer-readable program for causing the computer to execute the steps of:

measuring a time limit of each of ~~said~~ a plurality of modules based on a request signal requesting bus acquisition received for each of ~~[[a]]~~ the plurality of modules, ~~for requesting bus acquisition;~~

determining a priority of bus acquisition of said plurality of modules according to a time limit as measured in said ~~measurement~~ step of measuring; and

controlling acquisition of the bus for said plurality of modules based on the priority as determined in said ~~priority~~ determination step of determining priority.